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		Title Subscriber Line Interface Circuitry With Common Base...
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APPLICATION ELEMENTS <small>See MPEP chapter 600 concerning utility patent application contents.</small>		ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231	
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Docket Number (Optional)
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Applicant, Patentee, or Identifier: HEIN, et al.
Application or Patent No.: _____
Filed or Issued: June 30, 2000
Title: Subscriber Line Interface Circuitry With Common Base
Audio Isolation Stage

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UNITED STATES PATENT APPLICATION

FOR

SUBSCRIBER LINE INTERFACE CIRCUITRY
WITH COMMON BASE AUDIO ISOLATION STAGE

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SUBSCRIBER LINE INTERFACE CIRCUITRY

WITH COMMON BASE AUDIO ISOLATION STAGE

FIELD OF THE INVENTION

This invention relates to the field of telecommunications. In particular, this
5 invention is drawn to subscriber line interface circuitry.

BACKGROUND OF THE INVENTION

Subscriber line interface circuits are typically found in the central office exchange
of a telecommunications network. A subscriber line interface circuit (SLIC) provides a
communications interface between the digital switching network of a central office and an
10 analog subscriber line. The analog subscriber line connects to a subscriber station or
telephone instrument at a location remote from the central office exchange.

The analog subscriber line and subscriber equipment form a subscriber loop. The
interface requirements of an SLIC result in the need to provide relatively high voltages and
currents for control signaling with respect to the subscriber equipment on the subscriber
15 loop. Voiceband communications are low voltage analog signals on the subscriber loop.
Thus the SLIC must detect and transform low voltage analog signals into digital data for
transmitting communications received from the subscriber equipment to the digital
network. For bi-directional communication, the SLIC must also transform digital data
received from the digital network into low voltage analog signals for transmission on the
20 subscriber loop to the subscriber equipment. Strict gain and longitudinal balance control
are required for subscriber loop applications.

In order to meet the strict requirements, high precision high voltage amplifiers are frequently used for processing voiceband signals. The voiceband output signal may be driven as a voltage through a desired output impedance to the subscriber equipment.

Alternatively, the tip and ring lines of the subscriber loop are driven by currents

- 5 corresponding to the voiceband signal while the desired output impedance is synthesized.

Disadvantages of high precision high voltage amplifier solutions include the added cost and board area requirements for the SLIC.

SUMMARY OF THE INVENTION

In view of limitations of known systems and methods, a subscriber line interface circuit is described. A method of coupling an outgoing audio signal to the subscriber line includes the step of receiving the outgoing analog audio signal. The outgoing analog audio
5 signal corresponds to a transformed digital audio signal originating from a digital switching network. The method includes the step of coupling the outgoing audio signal to the subscriber line through a plurality of transistors coupled in a common base configuration. In one embodiment, the method includes the step of receiving linefeed driver control signals on the same signal lines as the outgoing audio signal. The linefeed driver control
10 signals control battery feed to the subscriber line.

In one embodiment, a subscriber line interface circuit apparatus includes a first circuit for coupling a received outgoing audio signal to a subscriber line. The first circuit couples the received outgoing audio signal to the subscriber line through a common base isolation stage. In various embodiments, the common base isolation stage comprises a
15 plurality of bipolar junction transistors coupled in a common base configuration or a plurality of field effect transistors coupled in a common gate configuration.

In one embodiment a subscriber line interface circuit includes a signal processor and a linefeed driver coupled to receive an outgoing audio signal from the signal processor. The linefeed driver couples the outgoing audio signal to the subscriber line through a
20 common base isolation stage. In one embodiment, the common base isolation stage comprises a plurality of bipolar junction transistors coupled in a common base configuration. In an alternative embodiment, the common base isolation stage comprises a

plurality of field effect transistors coupled in a common gate configuration. In various embodiments, the signal processor provides linefeed control signals to the linefeed driver on the same signal lines as the outgoing audio signals. The linefeed control signals control battery feed to the subscriber line.

5 Other features and advantages of the present invention will be apparent from the accompanying drawings and from the detailed description that follows.

BRIEF DESCRIPTION OF THE DRAWINGS

The present invention is illustrated by way of example and not limitation in the figures of the accompanying drawings, in which like references indicate similar elements and in which:

5 Figure 1 illustrates one embodiment of a central office exchange including a subscriber line interface circuit (SLIC) coupling subscriber equipment to a digital switching system.

Figure 2 illustrates a block diagram of an SLIC including a signal processor and a linefeed driver.

10 Figure 3A illustrates a circuit for driving an outgoing audio signal as a voltage through an output impedance.

Figure 3B illustrates a circuit for driving the audio output signal as a current through a synthesized output impedance.

Figure 4 illustrates one embodiment of a linefeed driver circuit.

15 Figures 5A illustrates the signal processor linefeed control providing an outgoing audio signal to a power circuitry portion of the linefeed driver circuitry.

Figure 5B illustrates a simplified model of transmission of the outgoing audio signal from the linefeed control portion of the signal processor onto the tip and ring nodes of the subscriber line through a common base isolation stage.

DETAILED DESCRIPTION

Figure 1 illustrates functional elements of one embodiment of a subscriber line interface circuit (SLIC) 110 typically associated with plain old telephone services (POTS) telephone lines. The subscriber line interface circuit (SLIC) provides an interface between
5 a digital switching network 120 of a local telephone company central exchange and a subscriber loop 132 including subscriber equipment 130.

The subscriber loop 132 is typically used for communicating analog data signals (e.g., voiceband communications) as well as subscriber loop "handshaking" or control signals. The analog data signals are typically on the order of 1 volt peak-to-peak (i.e.,
10 "small signal"). The subscriber loop control signals typically consist of a 48 VDC offset and an AC signal of 40-140 Vrms (i.e., "large signal"). The subscriber loop state is often specified in terms of the tip 180 and ring 190 portions of the subscriber loop.

The SLIC is expected to perform a number of functions often collectively referred to as the BORSCHT requirements. BORSCHT is an acronym for "battery feed,"
15 "overvoltage protection," "ring," "supervision," "codec," "hybrid," and "test."

Recent transformerless SLIC designs tend to distribute the functional requirements between two integrated circuits based on whether the functions are traditionally associated with the high voltage subscriber loop controls or the low voltage data processing. For example, in one embodiment, the codec is implemented in a low voltage
20 integrated circuit and the remaining functions (e.g., supervision) are implemented primarily in a high voltage integrated circuit such as a bipolar integrated circuit. Although this design tends to offer considerable space, weight, and power efficiencies over designs

requiring passive inductive components, this distribution of the functional requirements tends to result in a relatively expensive high voltage integrated circuit.

Figure 2 illustrates one embodiment of a SLIC wherein the BORSCHT functions are distributed between a signal processor 210 and a linefeed driver 220. Signal processor 210 is responsible for at least the ring control, supervision, codec, and hybrid functions. Signal processor 210 controls and interprets the large signal subscriber loop control signals as well as handling the small signal analog voiceband signals and the digital voiceband data. In one embodiment, the signal processor 210 is an integrated circuit.

In one embodiment, the signal processor includes a processor interface 214 to enable programmatic control of the signal processor 210. The processor interface effectively enables programmatic or dynamic control of battery control, battery feed state control, voiceband signal amplification and level shifting, longitudinal balance, ringing currents, and other subscriber loop control parameters as well as setting thresholds such as a ring trip detection thresholds and an off-hook detection threshold.

Signal processor 210 includes a codec for bi-directional transformation of the voiceband communications between the digital and analog domains as is well known in the art. The digital voiceband data is received from the digital switching network on interface 216. Within the signal processor, the digital voiceband data is coupled to a digital codec interface. An analog codec interface provides outgoing analog voiceband signals to the linefeed driver. The analog codec interface also receives incoming analog voiceband signals from the linefeed driver. The terms "incoming" and "outgoing" used in reference to the voiceband (i.e., audio) signal refer to the intended data flow from the perspective of the

digital switching network. Thus, incoming voiceband signals received from the subscriber line are transformed from analog to digital form and provided to the digital switching network. Outgoing voiceband signals from the digital switching network are transformed from digital to analog form and provided to the subscriber line for use by the subscriber equipment.

Signal processor 210 receives subscriber line state information from linefeed driver 220 as indicated by tip/ring sense 222. This information is used to generate control signals for linefeed driver 220 as indicated by linefeed driver control 212. In one embodiment, the linefeed driver control and outgoing analog voiceband signals are communicated on the same signal lines 212. Incoming analog voiceband signals are received by the signal processor on line 230.

Linefeed driver 220 maintains responsibility for battery feed to tip 280 and ring 290. Overvoltage protection is not explicitly illustrated, however, overvoltage protection can be provided by fuses incorporated into linefeed driver 220, if desired. Linefeed driver 220 includes sense circuitry to provide signal processor 210 with pre-determined sensed subscriber loop operating parameters as indicated by tip/ring sense 222. Signal processor 210 performs any necessary processing on the sensed parameters in order to determine the operational state of the subscriber loop. For example, differences or sums of sensed voltages and currents are performed as necessary by signal processor 210 rather than linefeed driver 220. Thus common mode and differential mode components (e.g., voltage and current) of the subscriber loop are calculated by the signal processor rather than the linefeed driver.

Linefeed driver 220 modifies the large signal tip and ring operating conditions in response to linefeed driver control 212 provided by signal processor 210. This arrangement enables the signal processor to perform processing as needed to handle the majority of the BORSCHT functions. For example, the supervisory functions of ring trip, ground key, and off-hook detection can be determined by signal processor 210 based on operating parameters provided by tip/ring sense 222.

Due to strict gain control and longitudinal balance requirements, a precise means of coupling voiceband signals to tip and ring is necessary. Figure 3A illustrates one embodiment of the a.c. signal components associated with the high precision high voltage amplifier providing the outgoing voiceband (i.e., audio) signal to tip and ring. The outgoing audio signal modeled as audio source 310 is driven as a voltage through a desired output impedance R_O using precision high voltage amplifier 320. Figure 3B illustrates an alternate approach. An outgoing audio signal 350 is driven as a current through a synthesized output impedance using precision high voltage transconductance amplifier 360 and differential error amplifier 370. The precision amplifiers are typically part of a high voltage integrated circuit which has disadvantages in terms of cost and board area required for implementation.

Figure 4 illustrates one embodiment of a SLIC linefeed driver 410. In one embodiment, the linefeed driver 410 is implemented as a number of discrete components. Linefeed driver 410 includes a voiceband sensing circuitry 420, line sensing circuitry 430, and power circuitry 440.

Voiceband circuitry 420 enables signals corresponding to voiceband communications to be retrieved from the subscriber loop. Nodes 424 and 428 serve to communicate voiceband signals from the subscriber loop to signal processor 210 (i.e., "incoming audio"). Capacitors CR and CT effectively provide AC coupling for the incoming audio signal from the subscriber loop to the signal processor while decoupling signal processor 210 from the DC offsets of the tip 480 and ring 490 nodes. Thus capacitors CR and CT effectively provide DC isolation of the incoming analog audio interface formed by nodes 424 and 428 from the subscriber loop. In the embodiment illustrated, voiceband circuitry 420 provides AC coupling of the incoming analog audio signal between the subscriber loop and the signal processor using only passive components.

Line sensing circuitry 430 enables signal processor 210 to determine the tip 480 and ring 490 node voltages as well as the subscriber loop current using sensing resistors RS1, RS2, RS3, and RS4. Resistors RT and RR are used to generate a voltage drop for determining the tip and ring currents. In one embodiment, line sensing circuitry 430 consists only of passive discrete components.

Referring to Figure 2, tip/ring sense 222 includes a sensed tip signal and a sensed ring signal. In one embodiment, the sensed tip signal includes first and second sensed tip voltages. Resistors RS1 and RS2 are used to sense the tip line voltage at each end of RT. Resistors RS1 and RS2 convert the sensed tip line voltages to currents suitable for handling by signal processor 210 at nodes 432 and 434. The difference between the first and second sensed tip voltages is proportional to the tip current. Likewise, the sensed

ring signal includes first and second sensed ring voltages. Resistors RS3 and RS4 similarly convert sensed ring line voltages at both ends of RR to currents suitable for handling by signal processor 210 at nodes 436 and 438. The difference between the first and second sensed ring voltages is proportional to the ring current. These calculations, however, can
5 be performed as necessary by the signal processor 210 rather than the linefeed driver 220 circuitry. In addition, these sensed parameters enable the signal processor 210 to determine the subscriber loop voltage and the subscriber loop common mode and differential mode currents.

Power circuitry 440 provides the battery feed and other relatively high voltage
10 functions to the subscriber loop in accordance with analog linefeed control signals provided by the signal processor 210 at nodes 442, 444, 446, and 448. Processing of the sensed parameters of the tip and ring lines for generating the linefeed control signals is handled exclusively by signal processor 210.

The subscriber loop current and the tip and ring voltages are controlled by
15 transistors Q1-Q6. In one embodiment, Q1-Q4 are PNP bipolar junction transistors and Q5-Q6 are NPN bipolar junction transistors. Given that the base terminals of Q1-Q4 are coupled to ground, nodes 442-448 need only be approximately 0.7 volts to turn on transistors Q1-Q4. Due to the small voltage drop between the base and emitters of Q1-Q4, control of the linefeed circuitry requires relatively low power and thus linefeed
20 driver control currents I1-I4 may be provided by a signal processor 210 implemented as a low voltage complementary metal oxide semiconductor (CMOS) integrated circuit.

Transistors Q1, Q4, and Q6 (and resistor R2) control the tip voltage 480. The tip voltage is increased by the application of control current I1 to Q1. The tip voltage (node 480) is decreased by the application of control current I4 to Q4. Thus control currents I1 and I4 effectively provide a tip control signal for manipulating the tip voltage at node 480.

5 Similarly, transistors Q2, Q3, and Q5 (and resistor R1) control the ring voltage 490. The application of control current I3 to Q3 increases the ring voltage. The ring voltage is decreased by the application of control current I2 to Q2. Control currents I2 and I3 effectively provide a ring control signal for manipulating the ring voltage at node 490.

10 Control currents I1-I4 thus effectively control the large signal subscriber loop current and tip and ring voltages. For example, the ringing signal can be generated by using the control signals at nodes 442-448 to periodically reverse the polarity of tip 480 with respect to ring 490 (i.e., battery polarity reversal) at the nominal ringing frequency.

Line sensing portion 430 enables signal processor 210 to determine the large signal
15 state of the subscriber loop without the need for intervening active circuitry or level shifters. In one embodiment, line sensing portion 430 comprises only passive discrete components. The linefeed control inputs 442-448 enable signal processor 210 to actively manage the large signal state of the subscriber loop. In particular, the large signal AC and DC components of the subscriber loop control protocol can now be controlled directly by
20 a low voltage integrated circuit. The large signal AC and DC control loops are effectively terminated at the signal processor 210.

In other words, the large signal AC and DC control loops are terminated at the low voltage integrated circuit. Thus signal processing and state determination such as off-hook, ring trip, and ring control formerly associated with high power analog circuitry can be handled predominately by a low voltage integrated circuit. In addition, the integrated circuit signal processor can handle processing of the small signal analog voiceband signals from the subscriber loop without the need for intervening active elements or level shifting circuitry.

In one embodiment, the outgoing analog audio signal is superimposed on the control currents I1 and I3 for power circuitry 440. Thus the audio signal and the linefeed control signals are provided on the same signal lines to the linefeed driver circuitry. The outgoing audio signal is communicated using nodes 442 and 446. One advantage of this configuration is that the termination impedance can be set by controlling currents I1 and I3. The use of a programmable signal processor effectively places the value of the termination impedance under programmatic control.

Figure 5A illustrates the linefeed control 520 portion of the signal processor 210 and the power circuitry 540 of the linefeed driver 410. Linefeed control 520 includes current sources 522, 524, 526, and 528 within the signal processor. The large signal components of currents I1-I4 are controlled by linefeed control 520. Transistors Q1 and Q3 receive the outgoing audio signal from the audio signal current source 530. Audio signal current source 530 provides a current (I_{OUT}) corresponding to the outgoing analog audio signal received from the codec. The outgoing analog audio signal originated as a digital audio signal from the digital network before being transformed by the codec.

During normal operation, a selected one of transistors Q5 and Q6 is "on."

Resistor R_L 560 represents the subscriber line impedance load. When transistor Q5 is on, Q2, Q5, and R1 effectively form a DC current source. Alternatively, when transistor Q6 is on, Q4, Q6, and R2 effectively form a DC current source. Figure 5B illustrates a model
5 of Figure 5A when transistor Q5 is on (normal battery feed). Q2, Q5, and R1 are modeled as DC current source 570. Current sources 522 and 526 are also large signal and thus DC current sources.

Transistors Q1 and Q3 are coupled in a common base configuration. Transistors Q1 and Q3 couple the outgoing audio signal received from the signal processor. The
10 common base isolation stage effectively isolates the signal processor from the DC offset of the tip 580 and ring 590 nodes. Audio current source 530 (I_{OUT}) manipulates I1 and I3 to put the outgoing audio signal onto the tip 580 and ring 590 nodes.

A DC bias current is established in Q1 and Q3 with non-precision low voltage and high voltage circuitry. The DC bias does not directly affect the audio gain or balance and
15 thus high precision is not required. Subscriber line impedance synthesis can be accomplished by providing sensed tip and ring voltages as feedback for the outgoing audio current source 530.

The gain through the common base stage is the average α (i.e., α_{AVG}) of Q1 and Q2. For typical transistor betas (β), α approaches 1.0. Thus there is approximately
20 unity gain between I_{OUT} and the AC component of the current in R_L corresponding to the

outgoing audio signal. I_{OUT} can be increased to accommodate lower transistor gains to maintain a fixed audio amplitude on tip and ring.

For example, if each of transistors Q1 and Q3 has a β between 100 and 200, the geometric mean of α is calculated as follows:

5

$$\alpha = \frac{\beta}{\beta + 1}$$
$$\alpha_{\text{mean}} = \sqrt{\alpha_1 \alpha_2} = \sqrt{\left(\frac{100}{101}\right)\left(\frac{200}{201}\right)} = 0.993$$

Current source 530 can be increased by $\frac{1}{0.993} = 1.007$ in order to give a net gain of 1.0.

The worst case gain variation would be calculated as follows:

$$\epsilon = \pm 20 \log \left(\frac{\alpha_{\text{min}}}{\alpha_{\text{mean}}} \right) = 20 \log \left(\frac{0.990}{0.993} \right) = \pm 0.03 \text{ db}$$

- 10 Thus the worst case gain variation is well within allowable gain variation requirements for subscriber line applications (± 0.15 to ± 0.50 db).

The balance requirement is a measure of the balance between the gains to tip and ring, respectively. This result is calculated as follows:

$$\text{balance} = 20 \log \left(\frac{\alpha_1 - \alpha_2}{\alpha_{\text{mean}}} \right) - 6.7 \text{ db}$$

- 15 For the example presented above, substitution of the values into the equation for balance yields:

$$\text{balance} = \left(\frac{0.995 - 0.990}{0.993} \right) - 6.7$$

= -53 db

Greater gain and balance control can be achieved through the use of transistors with higher or better matched betas. Alternatively, other configurations such as Darlington pairs can be used to achieve a greater β . Different types of transistors such as metal oxide

5 semiconductor or junction field effect transistors (i.e., MOSFET or JFET) can be used. The term "common base" includes "common gate" equivalents for MOSFET and JFET transistors. Thus a "common base isolation stage" is intended to include field effect transistors coupled in a common gate configuration.

10 In the preceding detailed description, the invention is described with reference to specific exemplary embodiments thereof. Various modifications and changes may be made thereto without departing from the broader spirit and scope of the invention as set forth in the claims. The specification and drawings are, accordingly, to be regarded in an illustrative rather than a restrictive sense.

CLAIMS

What is claimed is:

- 1 1. A method comprising the steps of:
 - 2 a) receiving an outgoing audio signal; and
 - 3 b) coupling the audio signal to a subscriber line through a plurality of
 - 4 transistors coupled in a common base configuration.
- 1 2. The method of claim 1 further comprising the step of:
 - 2 c) receiving linefeed driver control signals for controlling battery feed to the
 - 3 subscriber line, wherein the outgoing audio signal and the linefeed driver control signals are
 - 4 received on the same signal lines.
- 1 3. The method of claim 1 wherein the plurality of transistors comprises bipolar
- 2 junction transistors.
- 1 4. The method of claim 1 wherein the plurality of transistors comprises field effect
- 2 transistors, wherein the common base configuration is a common gate configuration.
- 1 5. A method comprising the steps of:
 - 2 a) receiving linefeed driver control signals and outgoing audio signals on a
 - 3 same plurality of signal lines; and

4 b) providing the outgoing audio signals to a subscriber line through a common
5 base isolation stage.

1 6. The method of claim 5 further comprising the step of:

2 b) controlling a battery feed to a tip node and a ring node of the subscriber
3 line in accordance with the linefeed driver control signals.

1 7. The method of claim 5 wherein the common base isolation stage comprises a
2 plurality of bipolar junction transistors coupled in a common base configuration.

1 8. The method of claim 5 wherein the common base isolation stage comprises a
2 plurality of field effect transistors coupled in a common gate configuration.

1 9. A subscriber line interface circuit apparatus, comprising:
2 a first circuit for coupling a received outgoing audio signal to a subscriber line,
3 wherein the first circuit couples the received outgoing audio signal to the subscriber line
4 through a common base isolation stage.

1 10. The apparatus of claim 9 wherein the first circuit comprises a plurality of bipolar
2 junction transistors coupled in a common base configuration.

1 11. The apparatus of claim 9, wherein the first circuit comprises a plurality of field
2 effect transistors coupled in a common gate configuration.

1 12. The apparatus of claim 9 wherein the first circuit comprises:
2 a tip control circuit, wherein the tip control circuit increases a tip node voltage in
3 response to a first tip control signal, wherein the tip control circuit decreases a tip node
4 voltage in response to a second tip control signal; and
5 a ring control circuit wherein the ring control circuit increases a ring node voltage in
6 response to a first ring control signal, wherein the ring control circuit decreases a ring node
7 voltage in response to a second ring control signal.

1 13. The linefeed driver of claim 12 wherein the tip control circuit comprises:
2 a first transistor of a first type having an emitter coupled to receive the first tip
3 control signal;
4 a second transistor of the first type having an emitter coupled to receive the
5 second tip control signal, wherein a base of each of the first and second transistors is
6 coupled to a first node as a signal ground;
7 a third transistor of a second type having a collector coupled to a collector of the
8 first transistor and an emitter coupled to a second node;
9 a resistor having a first end coupled to the second node, a second end of the
10 resistor coupled to a base of the third transistor and a collector of the second transistor.

1 14. The subscriber line linefeed driver of claim 13 wherein the first type is a PNP
2 bipolar junction transistor, wherein the second type is an NPN bipolar junction
3 transistor.

1 15. A subscriber line interface circuit apparatus, comprising:
2 a signal processor providing an outgoing audio signal; and
3 a linefeed driver coupled to receive the outgoing audio signal, wherein the linefeed
4 driver couples the received outgoing audio signal to a subscriber line through a common
5 base isolation stage.

1 16. The apparatus of claim 15 wherein the common base isolation stage comprises a
2 plurality of bipolar junction transistors coupled in a common base configuration.

1 17. The apparatus of claim 15 wherein the common base isolation stage comprises a
2 plurality of field effect transistors coupled in a common gate configuration.

1 18. The linefeed driver of claim 15 wherein the linefeed driver comprises:
2 a tip control circuit, wherein the tip control circuit increases a tip node voltage in
3 response to a first tip control signal, wherein the tip control circuit decreases a tip node
4 voltage in response to a second tip control signal; and
5 a ring control circuit wherein the ring control circuit increases a ring node voltage in
6 response to a first ring control signal, wherein the ring control circuit decreases a ring node

7 voltage in response to a second ring control signal, wherein the signal processor provides
8 the first and second tip control signals and the first and second ring control signals.

1 19. The linefeed driver of claim 18 wherein the tip control circuit comprises:

2 a first transistor of a first type having an emitter coupled to receive the first tip
3 control signal;

4 a second transistor of the first type having an emitter coupled to receive the
5 second tip control signal, wherein a base of each of the first and second transistors is
6 coupled to a first node as a signal ground;

7 a third transistor of a second type having a collector coupled to a collector of the
8 first transistor and an emitter coupled to a second node; and

9 a resistor having a first end coupled to the second node, a second end of the
10 resistor coupled to a base of the third transistor and a collector of the second transistor.

1 20. The linefeed driver of claim 19 wherein the first type is a PNP bipolar junction
2 transistor, wherein the second type is an NPN bipolar junction transistor.

ABSTRACT OF THE DISCLOSURE

Methods and apparatus for coupling outgoing analog audio signals to a subscriber line are described. One method includes the step of receiving the outgoing audio signal.

The outgoing audio signal is coupled to the subscriber line through a plurality of

5 transistors coupled in a common base configuration. In one embodiment, linefeed driver control signals for controlling battery feed to the subscriber line are received on the same signal lines as the outgoing audio signal. A subscriber line interface circuit apparatus

includes a first circuit for coupling a received outgoing audio signal to a subscriber line.

The first circuit couples the received outgoing audio signal to the subscriber line through a

10 common base isolation stage. In various embodiments, the common base isolation stage comprises a plurality of bipolar junction transistors coupled in a common base configuration or a plurality of field effect transistors coupled in a common gate configuration.

FIG. 1 is a block diagram of a subscriber loop circuit.

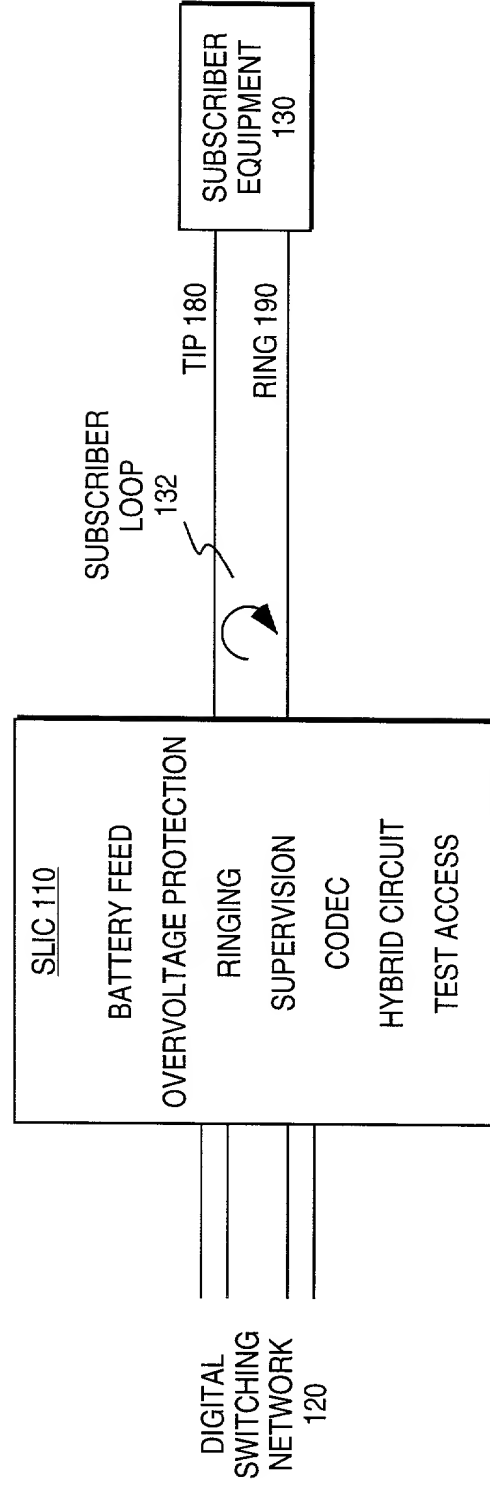


FIG. 1

FIG. 2 is a block diagram of a system 200 for processing a voice signal. The system 200 includes a signal processor 210 and a linefeed driver 220. The signal processor 210 is connected to a processor interface 214 and a digital voiceband 216. The linefeed driver 220 is connected to a battery and has two output lines, TIP 280 and RING 290. The signal processor 210 and the linefeed driver 220 are connected via three signal lines: a linefeed driver control/analog voiceband signal out 212, an analog voiceband signal in 230, and a tip/ring sense 222.

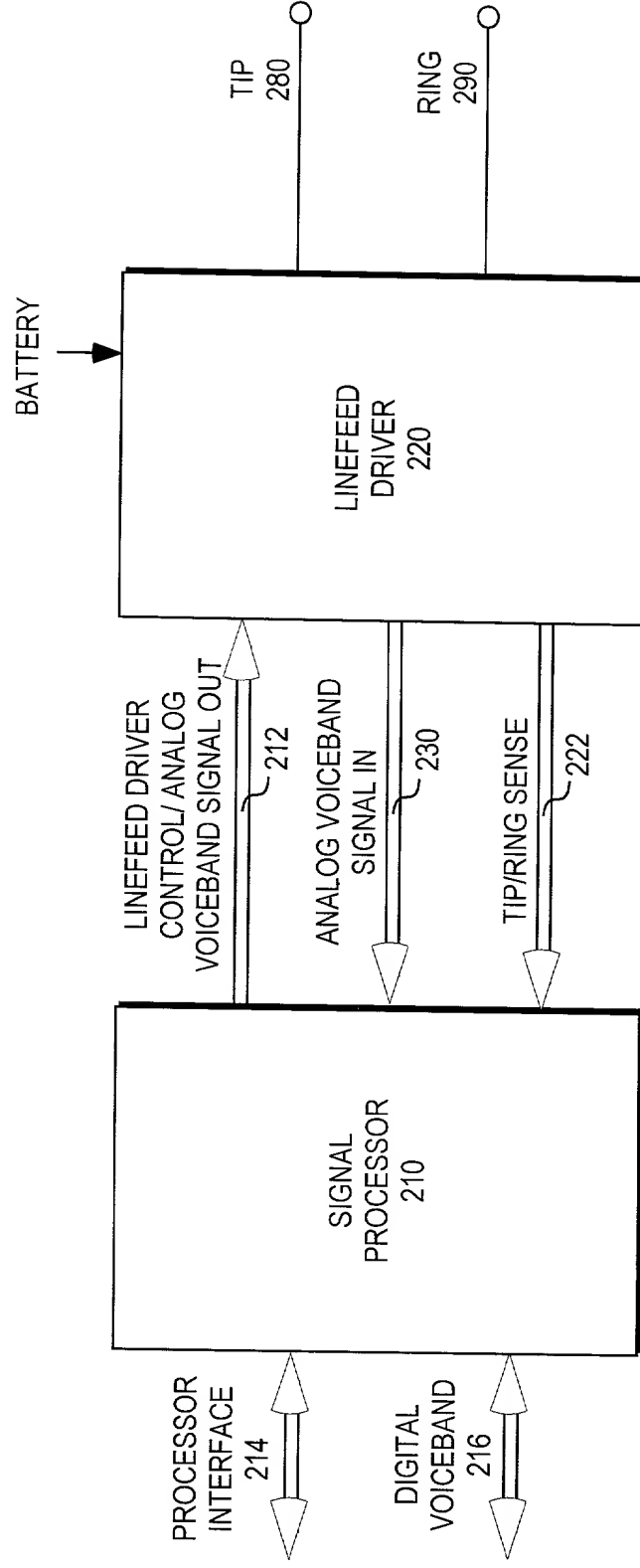


FIG. 2

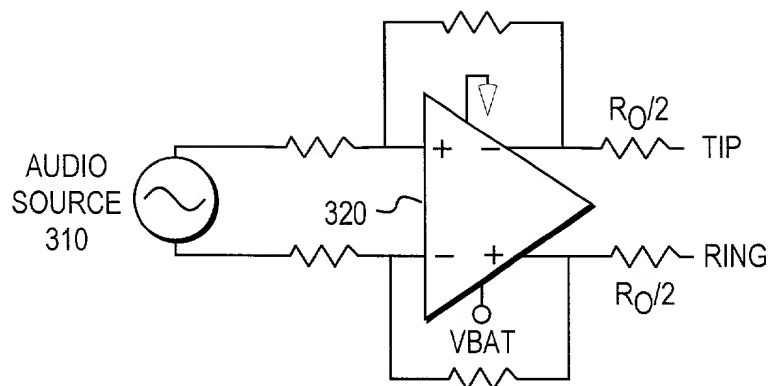


FIG. 3A

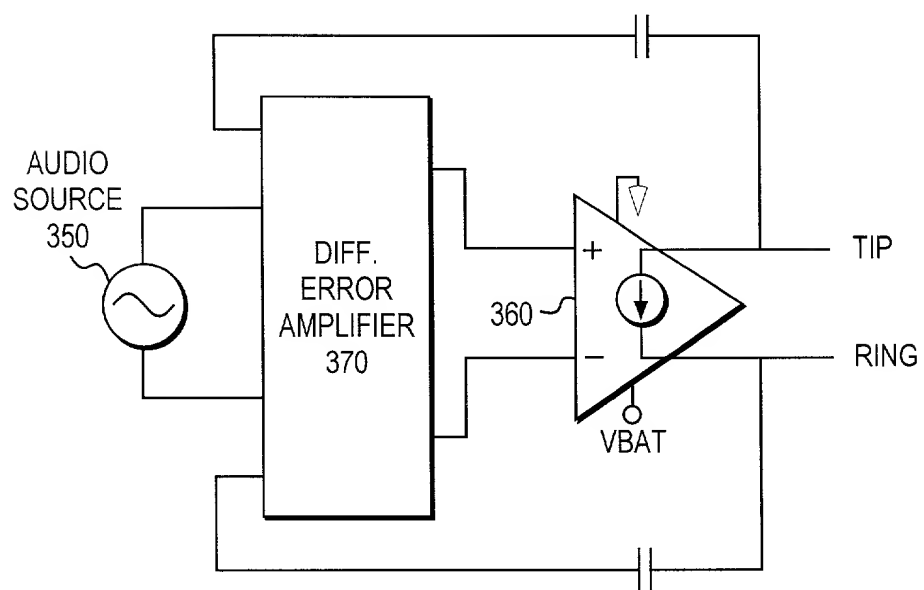


FIG. 3B

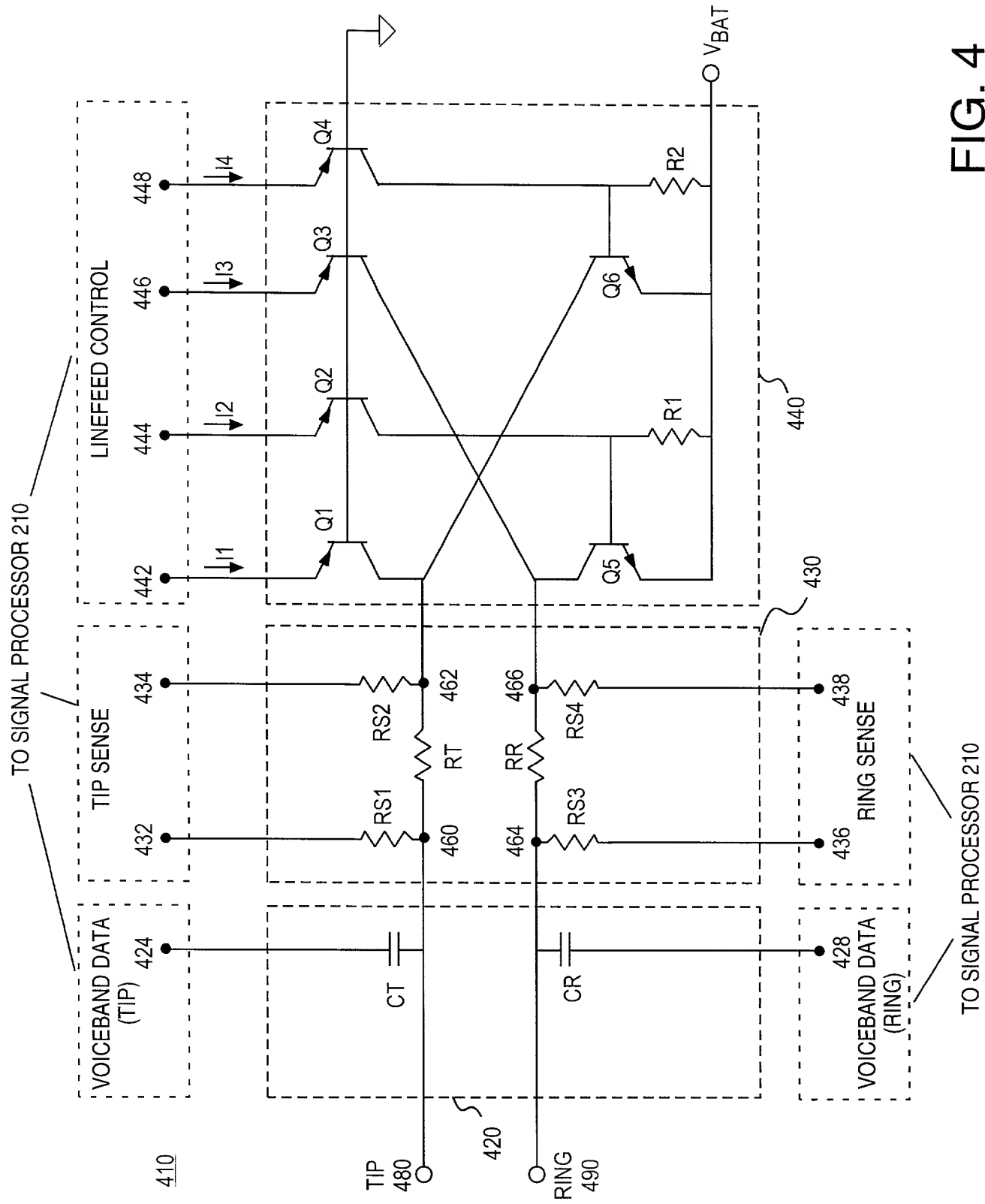


FIG. 4

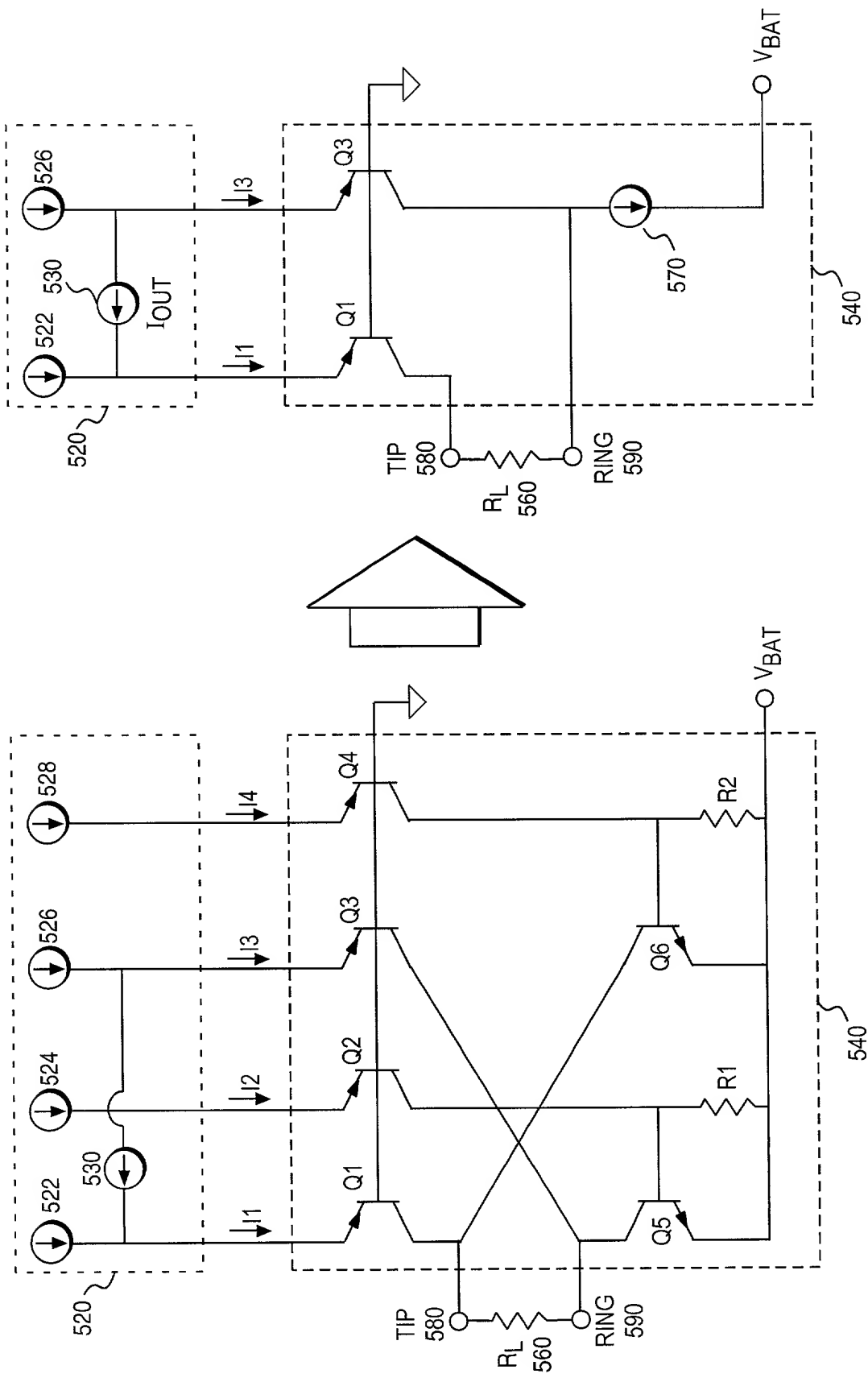


FIG. 5A

FIG. 5B

DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

**SUBSCRIBER LINE INTERFACE CIRCUITRY
WITH COMMON BASE AUDIO ISOLATION STAGE**

the specification of which

 X is attached hereto.
 was filed on _____ as
United States Application Number _____
or PCT International Application Number _____
and was amended on _____.
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above. I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Foreign Application(s) and/or Claim of Foreign Priority

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Country	Application No.	Date Filed	35 U.S.C. §119 Priority Claimed

Provisional Application Claim of Priority

I hereby claim the benefit under title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below:

Application No.	Filing Date

U.S. Priority Claim

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application No.	Filing Date	Status

Power of Attorney

I hereby appoint the following attorney(s) and/or agent(s) with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

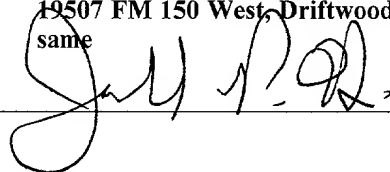
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Date

6/26/2000